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FEB 22 2005
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TRANSMITTAL LETTER
(General - Patent Pending)

Docket No.
00750484AA

In Re Application Of: Doris et al.

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|-------------------------------|---------------------------|----------------------|-----------------------|------------------------|--------------------------|
| Application No. 10/735,736 | Filing Date 12/16/2003 | Examiner Y. Huynh | Customer No. 30743 | Group Art Unit 2813 | Confirmation No. 3646 |
|-------------------------------|---------------------------|----------------------|-----------------------|------------------------|--------------------------|

Title: ULTRA-THIN SI CHANNEL CMOS WITH IMPROVED SERIES RESISTANCE

COMMISSIONER FOR PATENTS:

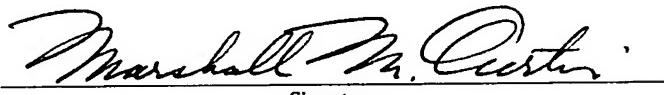
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Signature

Dated: February 22, 2005

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FIS920030123US1 (00750484AA)

PATENT APPLICATION

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Bruce B. Doris et al. Conf. No.: 3646

Serial No.: 10/735,736 Group Art Unit: 2813

Filed: December 16, 2003 Examiner: Y. Huynh

For: ULTRA-THIN SI CHANNEL CMOS WITH IMPROVED SERIES
RESISTANCE

Commissioner for Patents
United States Patent and Trademark Office
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RESPONSE TO REQUIREMENT FOR RESTRICTION

Sir:

In response to the Office Action mailed January 27, 2005, Applicants provisionally elect the invention, as identified by the Examiner, of Group I, method claims 1 - 8, with traverse, for further action on the merits.

No amendments to the specification or claims are presented in this paper.